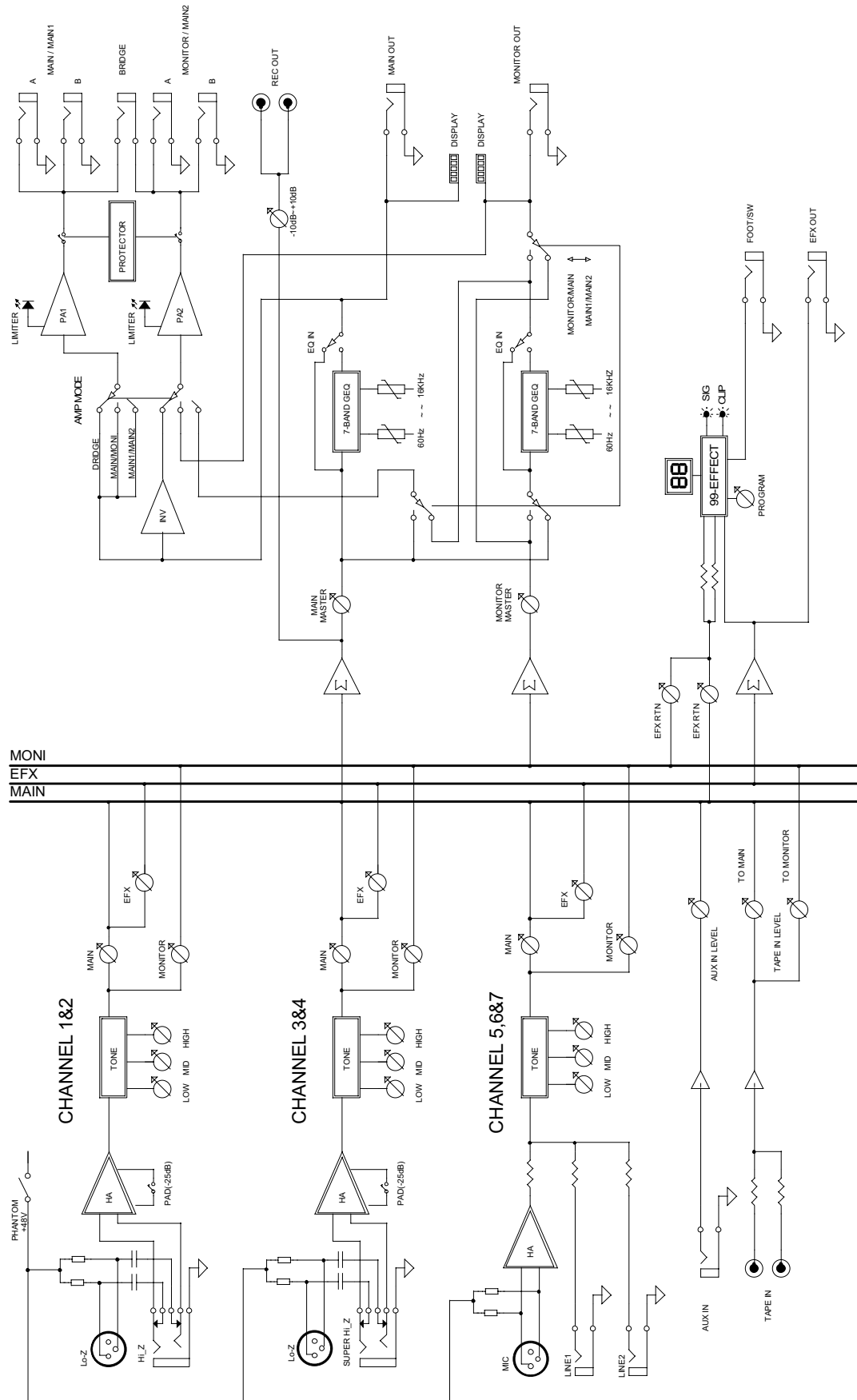
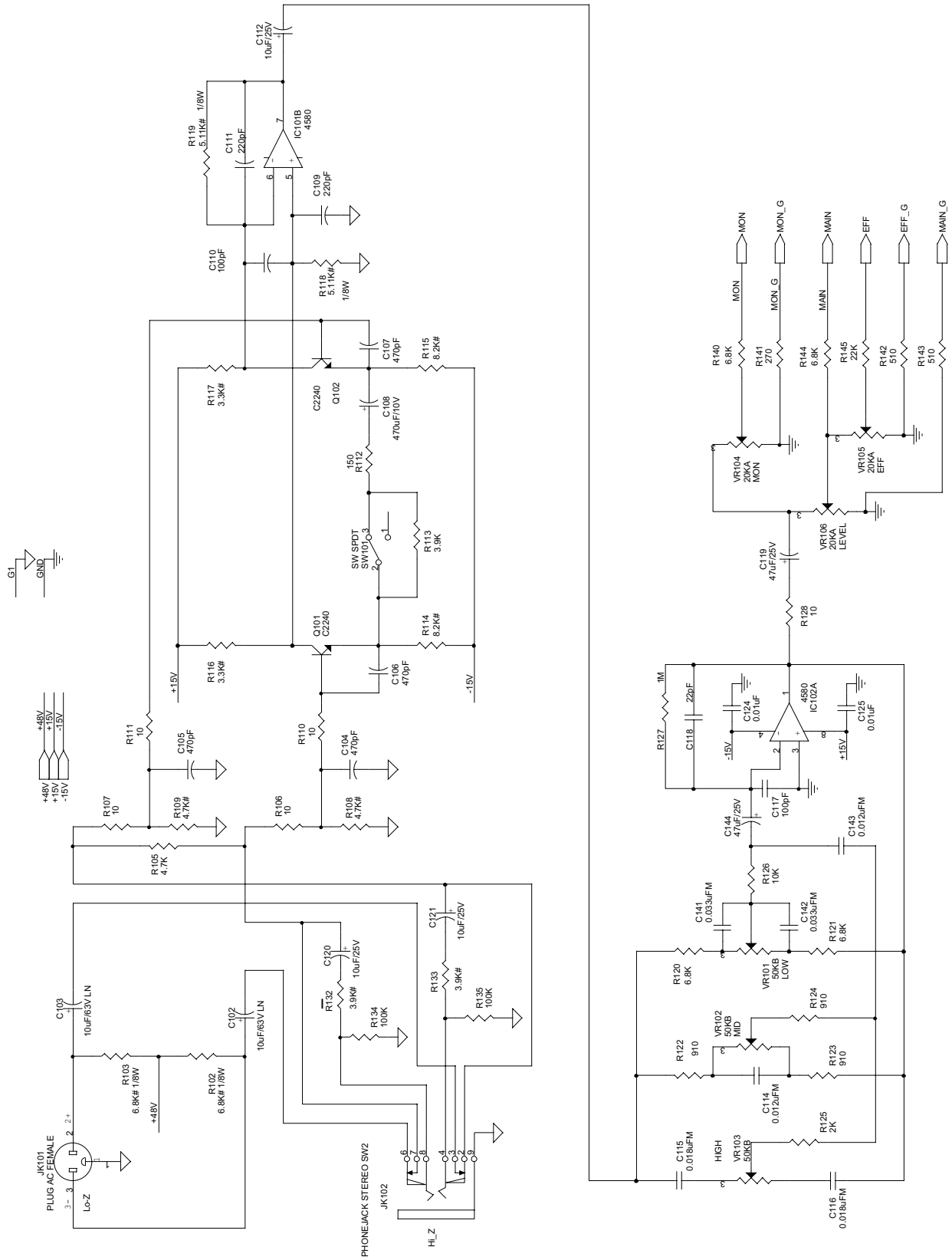


BLOCK

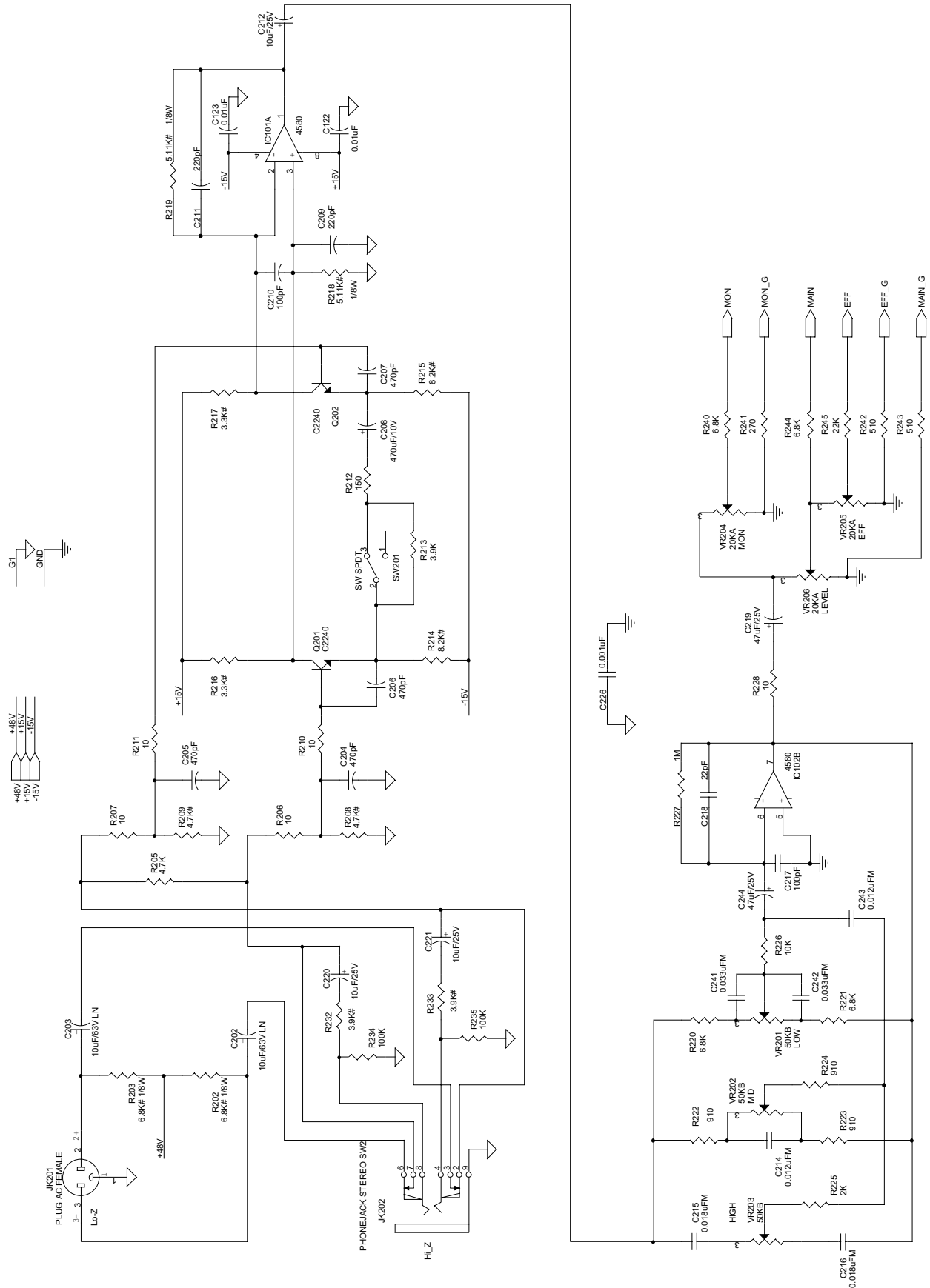


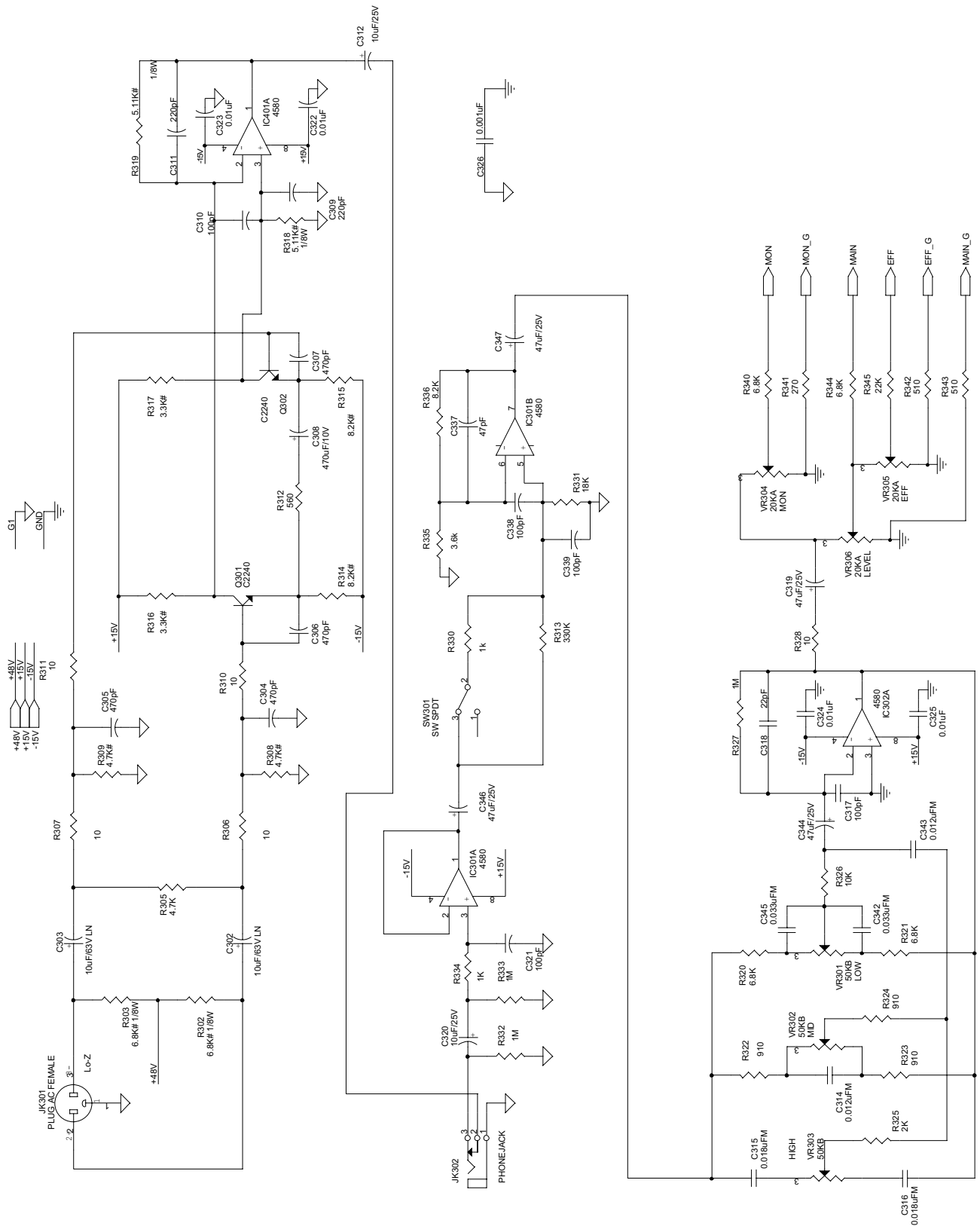


CH_1 IN/OUT

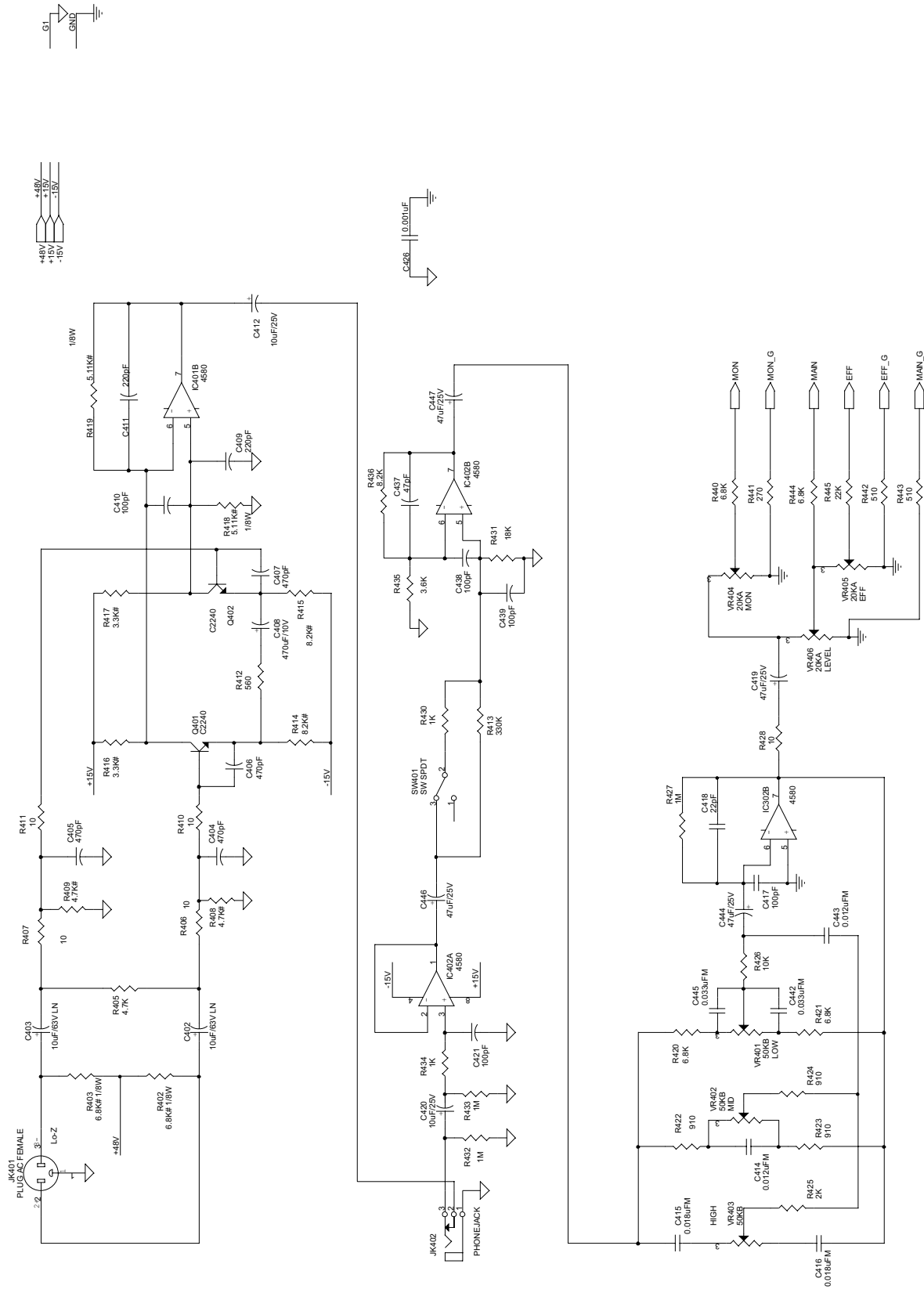


CH2

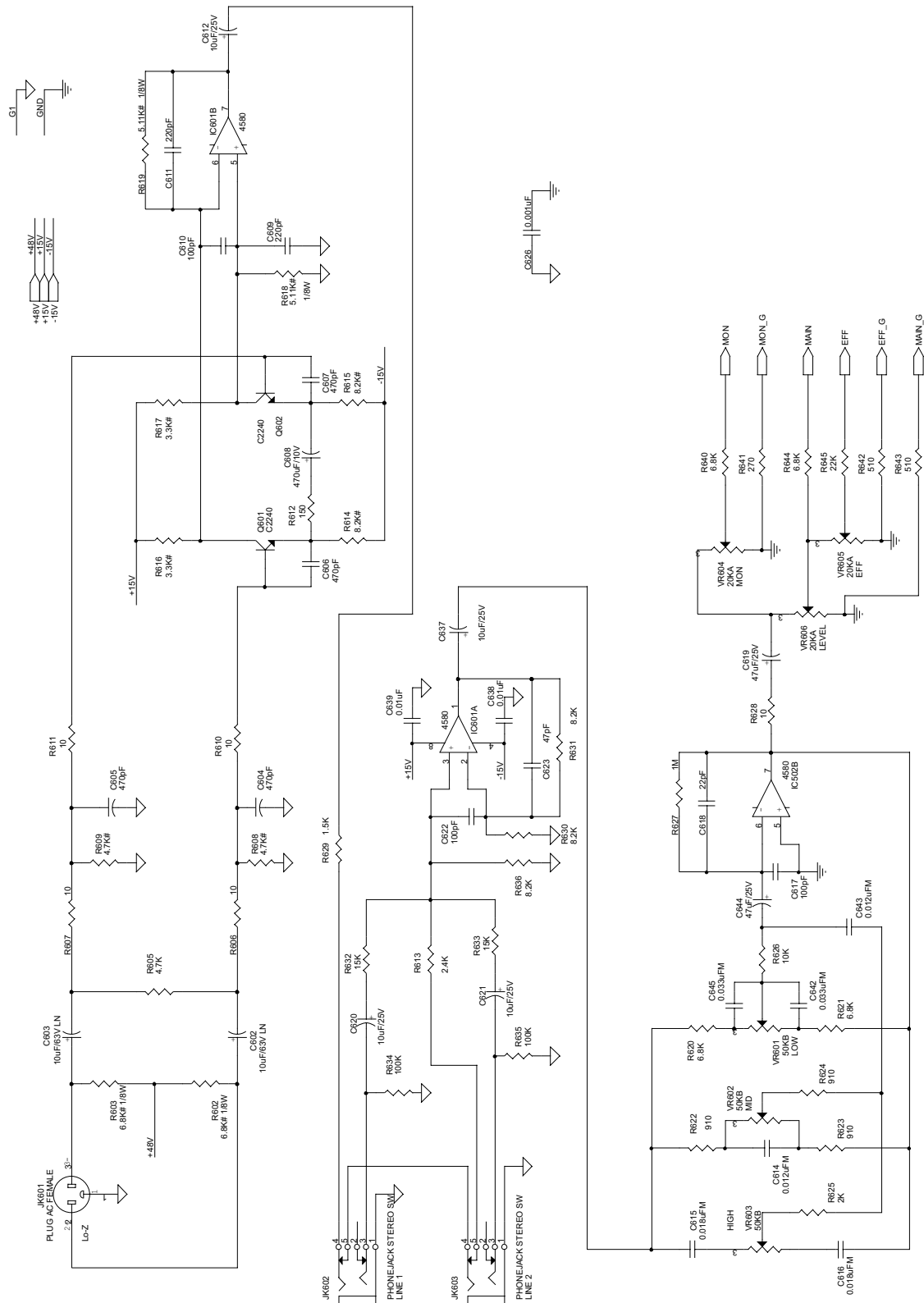


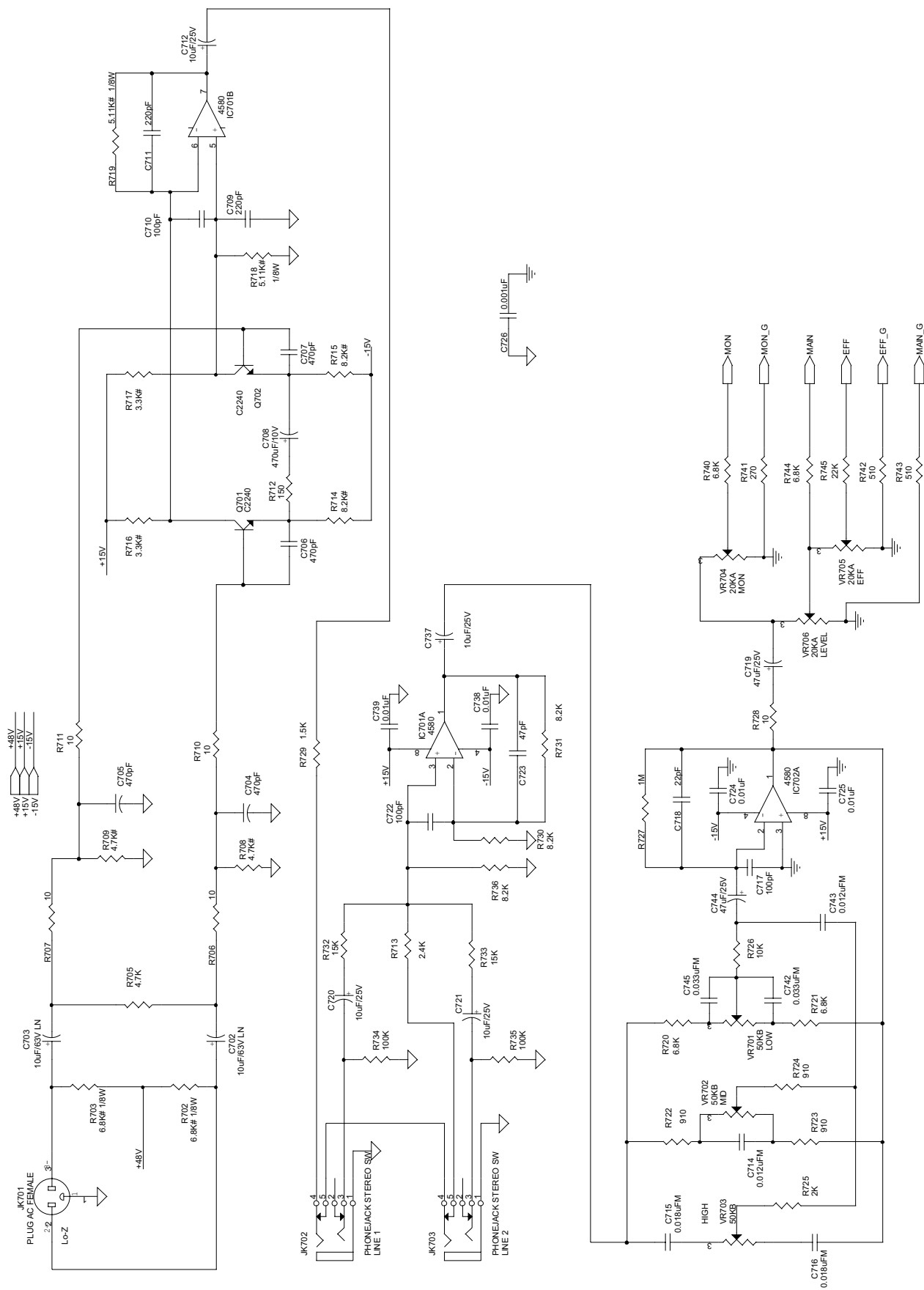


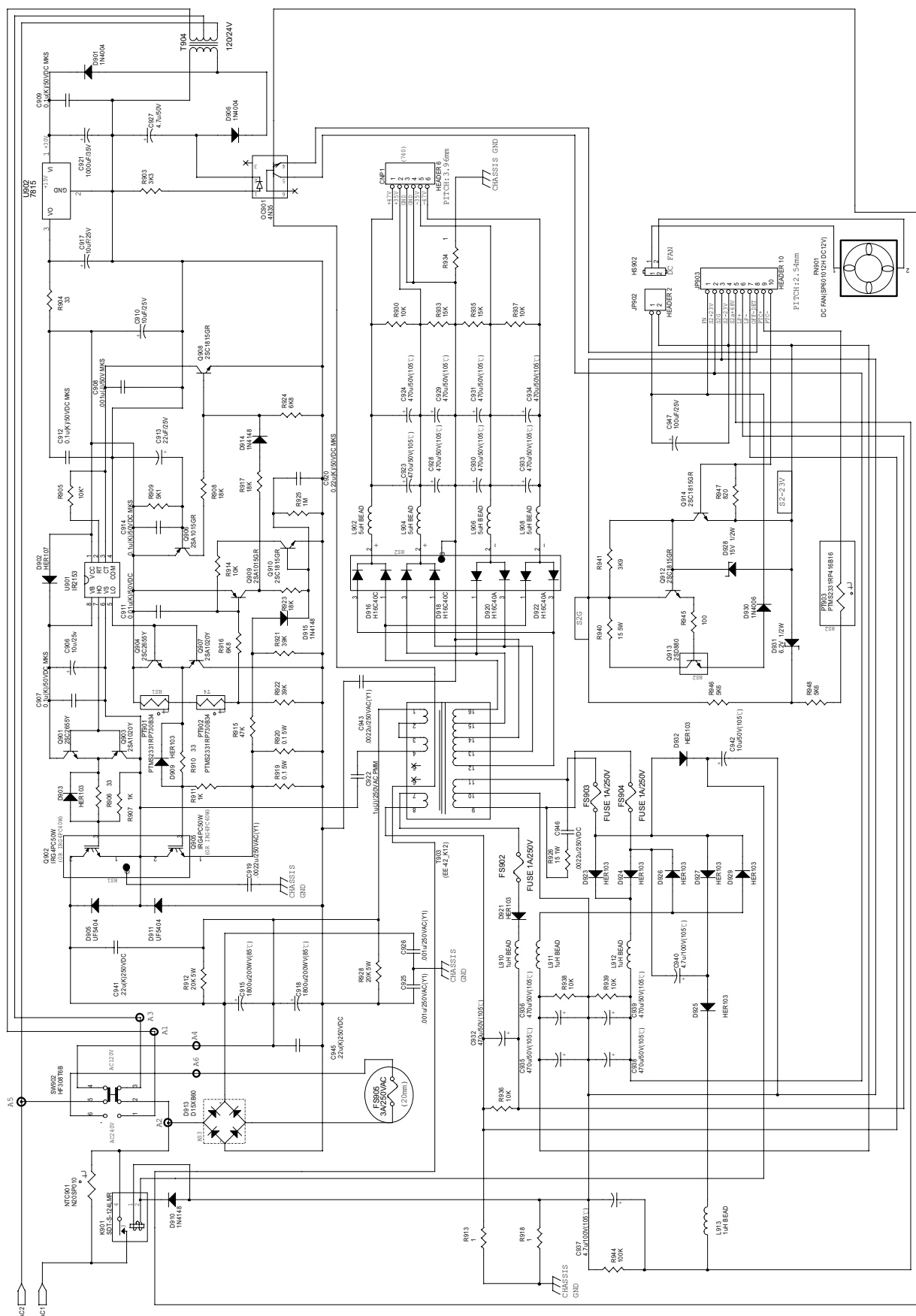
CH4



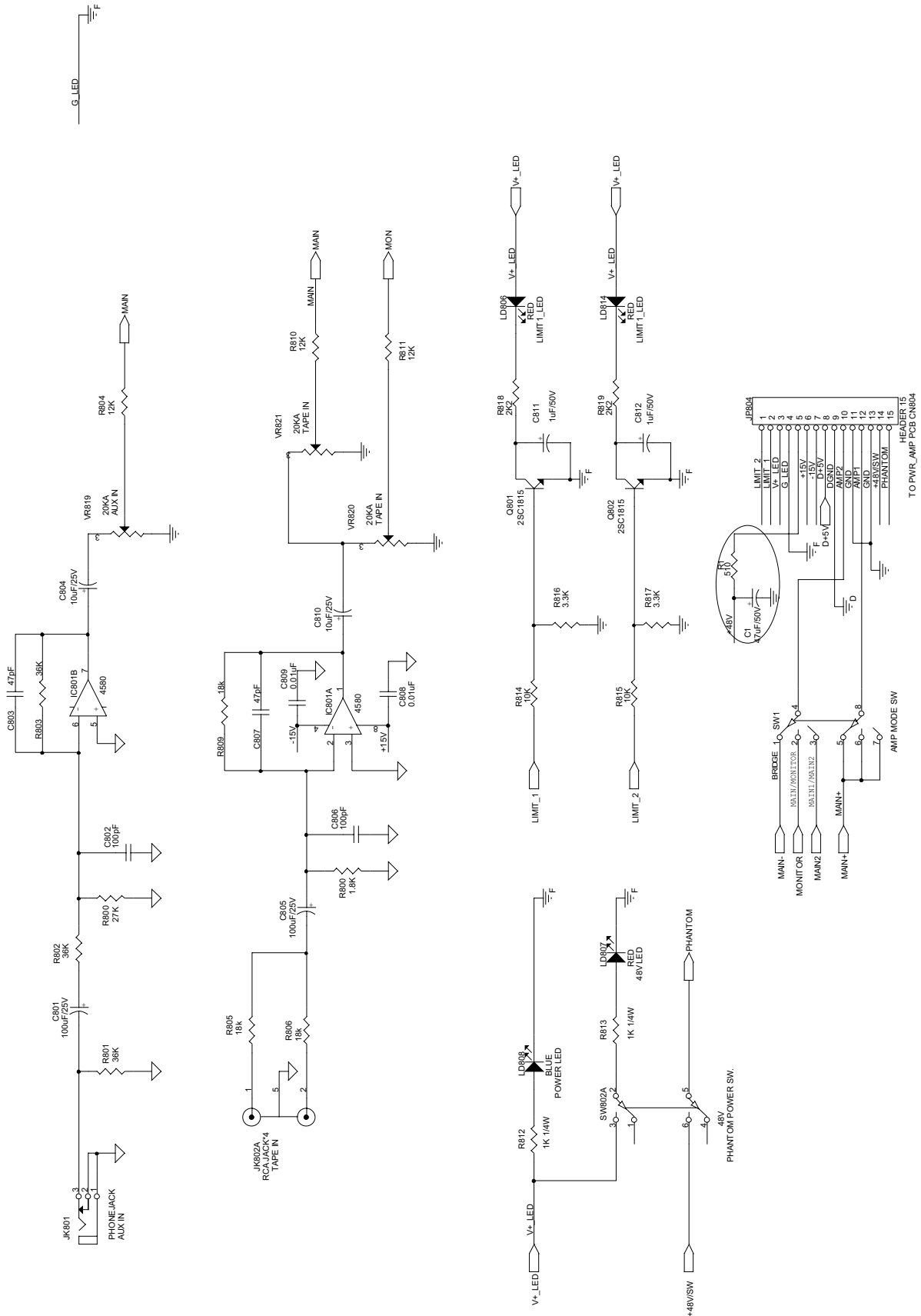




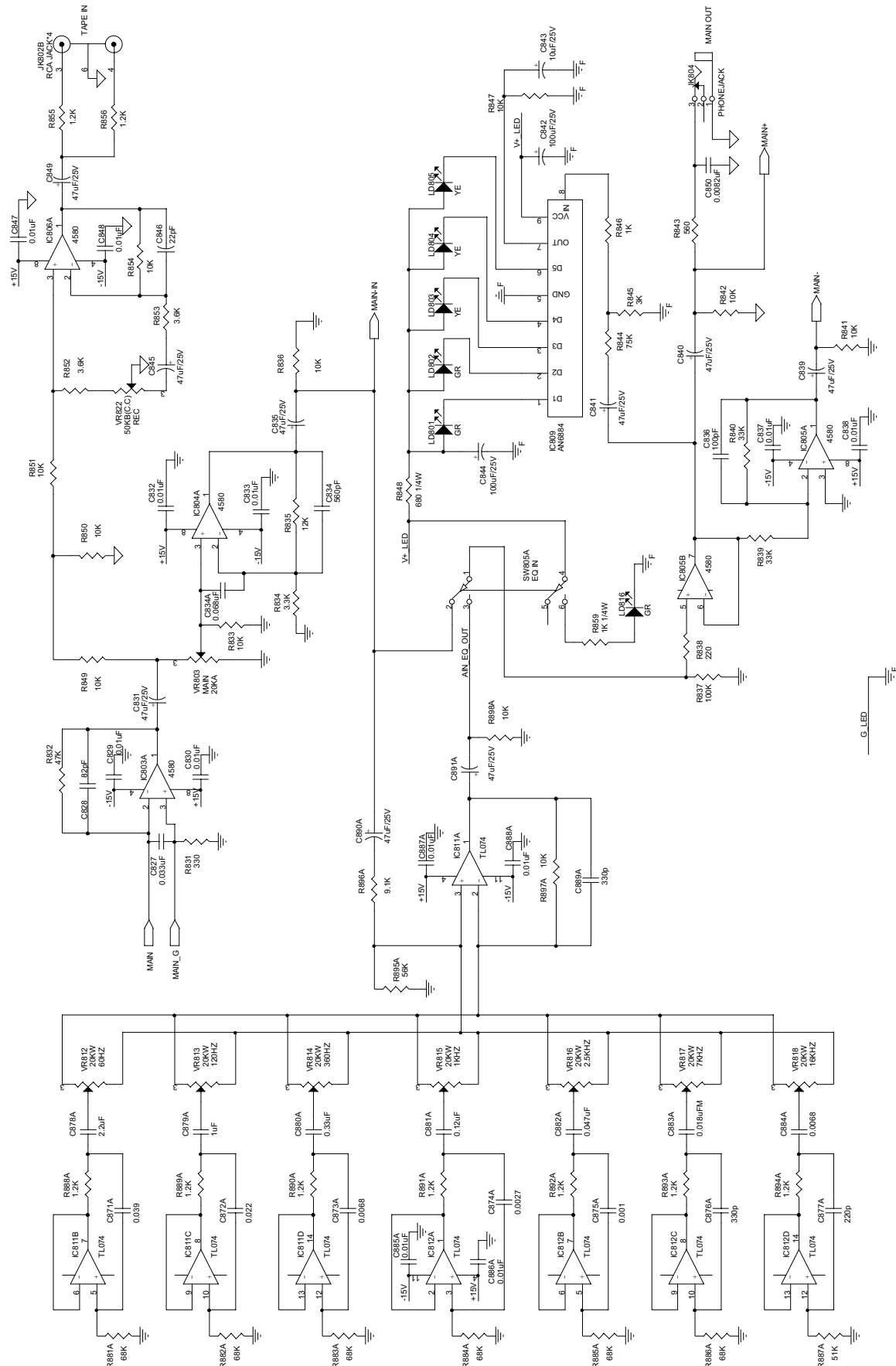


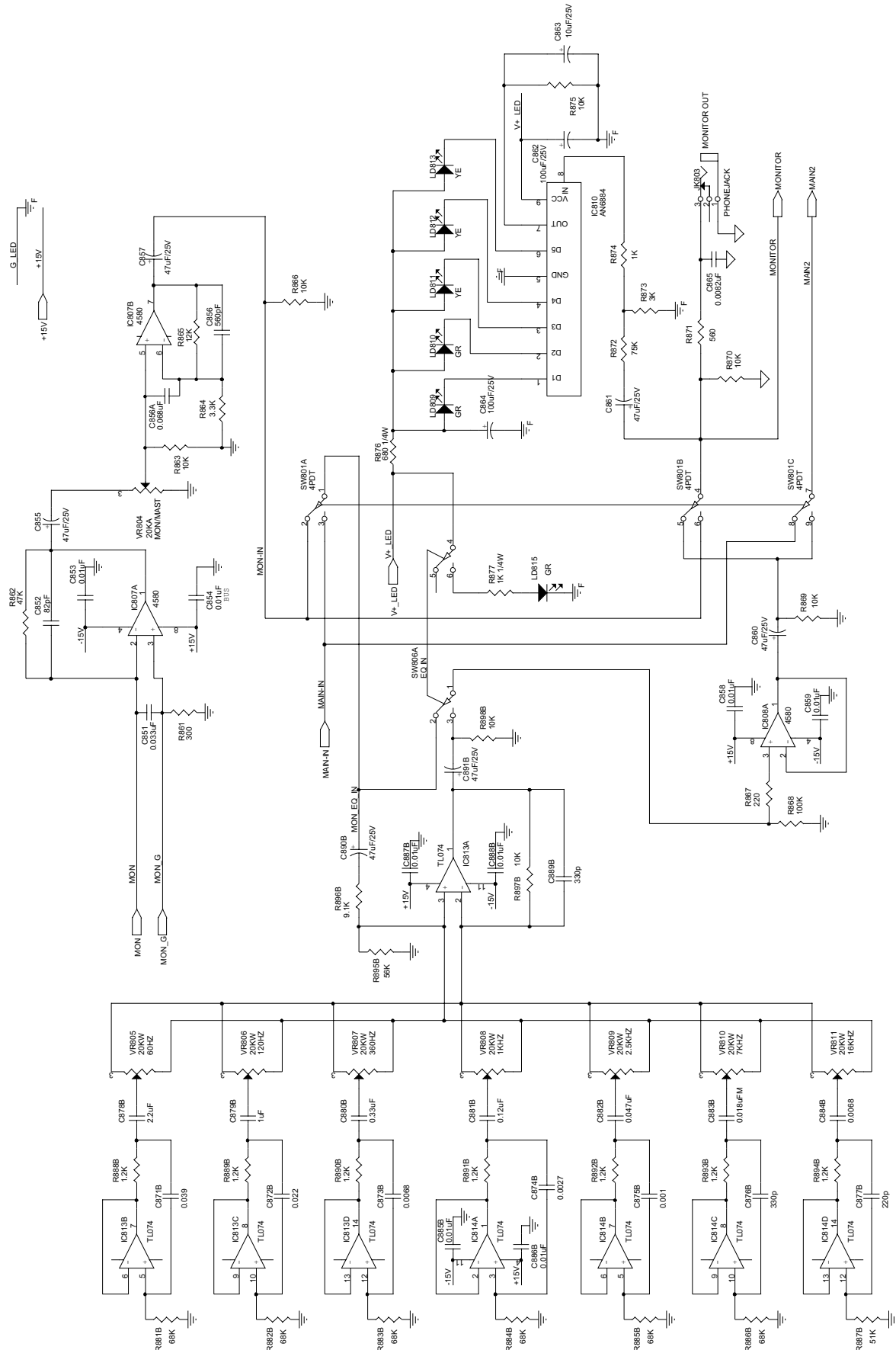


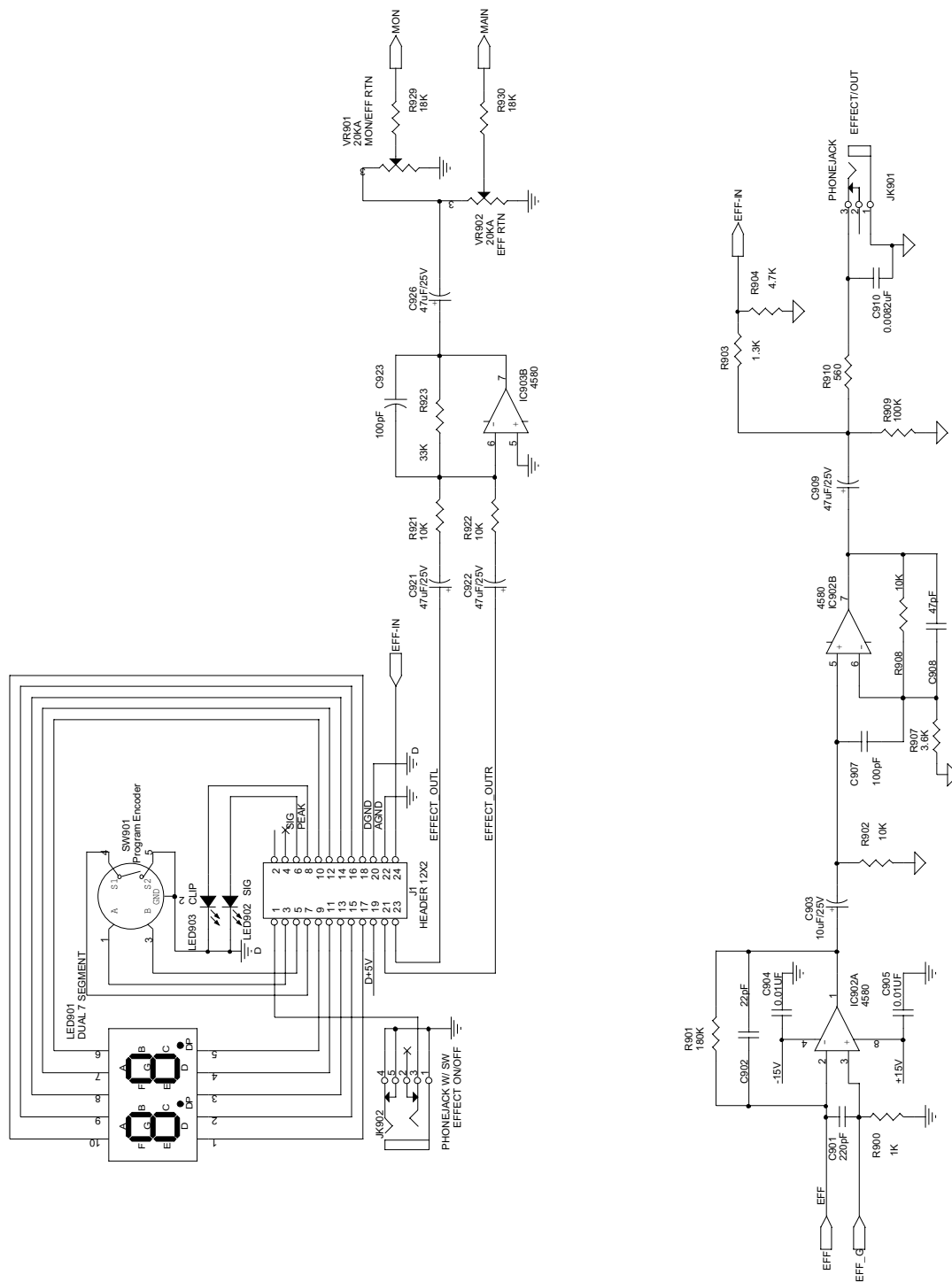
AUX_TAP



MAIN





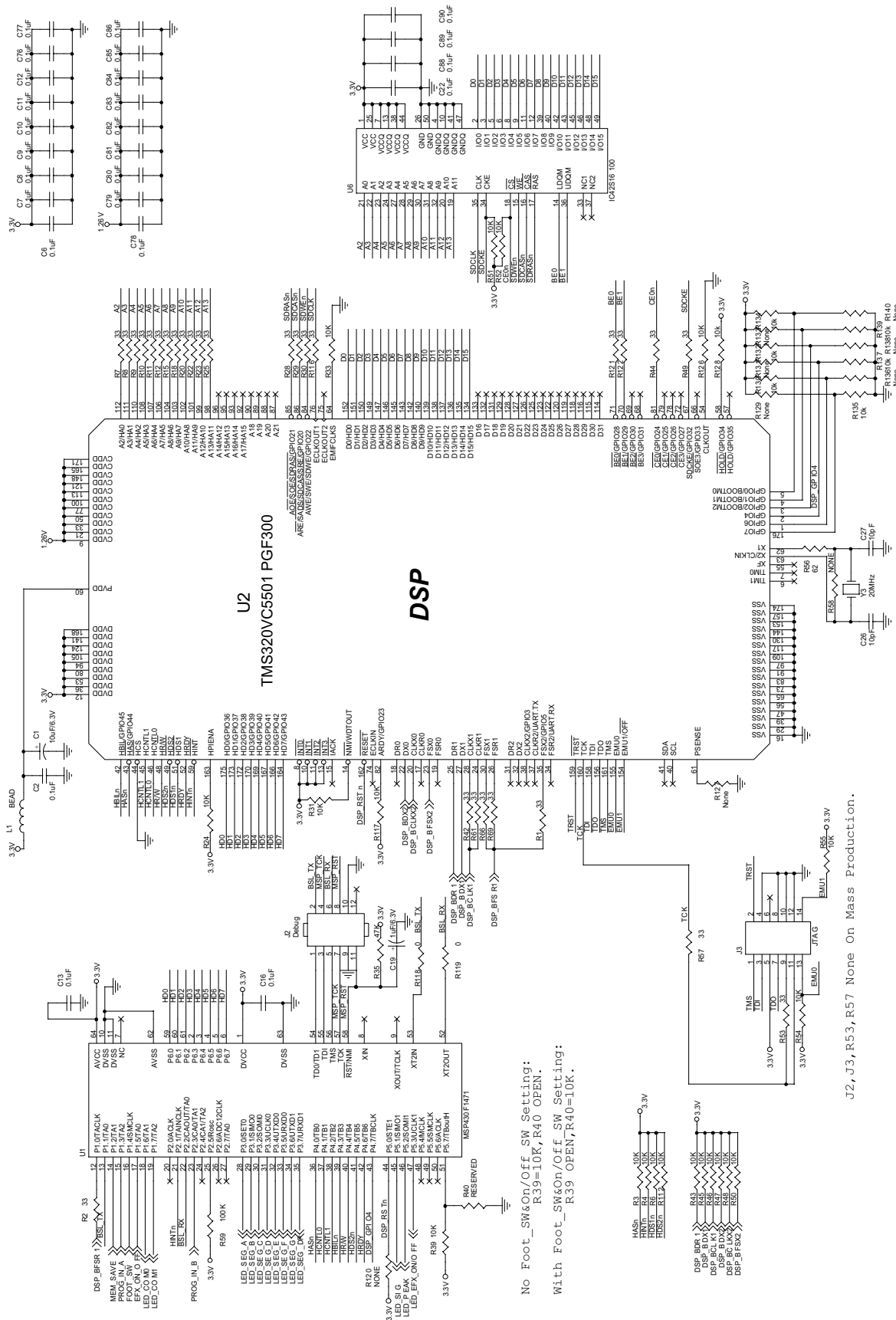
EFF_CTL







DSP SEETION



J2, J3, R53, R57 None On Mass Production.

ANALOG I/O

