INPUTS (81301 / 81302)

16-(+48V)
15-(PFL-C)
14-(+15V)
13-(EFF)
12-(GND)
11-(AUX1)
10-(GND)
9-(AUX2-POST)
8-(GND)
7-(AUX2-PRE)
6-(GND)
5-(RIGHT)
4-(-15V)
3-(LEFT)
2-(PFL-R)
1-(PFL-L)
CN1

MOTHERLOOM MO_BUS[1..16]

MASTER LEFT (82203)

16-(GND-D)
15-(REL-DRIVE)
14-(GND-D)
13-(MONO-SUM)
12-(GND-D)
11-(TAPE-MONO)
10-(GND-D)
9-(EFF-RET-RI)
8-(MONO-OUT-)
7-(EFF-RET-LE)
6-(MONO-OUT+)
5-(PFL-DIS)
4-(EFF-MONO)
3-(TAPE-SE-RI)
2-(GND-TAPE)
1-(TAPE-RET-RI)
CN2

CN1

CN4/5

1-(+48V)
2-(TAPE-RET-RI)
3-(GND)
4-(TAPE-RET-LE)
5-(GND)
6-(AUX1-RET-RI)
7-(GND)
8-(AUX1-RET-LE)
9-(GND)
10-(TAPE-SE-LE)
11-(GND)
12-(TAPE-SE-RI)
13-(GND)
14-(AUX1-SE)
15-(DIS-LE)
16-(MA-LE)
CN3

I/O/LE[1..16]

IN/OUT JACKS
(86 203)

CN1

4-(PROTECT-LED)
3-(CLI-RI)
2-(CLI-LE)
1-(+15V)
CN4

PRO[1..4]

16-(AUX2-RET-LE)
15-(DIS-LE)
14-(AUX2-RET-RI)
13-(GND)
12-(DIS-RI)
11-(GND)
10-(MONO-OUT-)
9-(GND)
8-(MONO-OUT+)
7-(GND)
6-(AUX2-SE+)
5-(GND)
4-(AUX2-SE-)
3-(GND)
2-(MA-RI)
1-(GND)
CN2

I/O/RI[1..16]

+48V

CN11

CN3

POWIN[1..10]

10-(IN-RI)
9-(GND)
8-(IN-LE)
7-(GND)
6-(-15V)
5-(+15V)
4-(NC)
3-(CLIP-LE)
2-(CLIP-RI)
1-(MUTE-DRIVE)
CN3

POWER AMPS
(84 153)

8-(MUTE DRIVE)
7-(-15V)
6-(-15V)
5-(GND)
4-(GND)
3-(GND)
2-(+15V)
1-(+15V)
CN1

PO_POW[1..8]

8-(REL-DRIVE)
7-(+UB-RELAY)
6-(+15V)
5-(NC)
4-(TEMP-SENSE)
3-(PROTECT-SENSE)
2-(NC)
1-(TRIAC-DRIVE)
CN5

O_CONT[1..8]

-UB
GND
+UB

CN2

CN3

+UB
CN10

LOW VOLTAGE
POWER SUPPLY
& TURN ON DELAY
(85 235)

HIGH VOLTAGE
POWER SUPPLY
(85 236)

CN5

CN6

MAINS
TRANSFORMER
DCN 348 529

MASTER RIGHT (82204)

CN3

CN1

CN2

MASTER MONO (82205)

CN2

CN1

MASTER EFFECT (83096)

CN3

CN1

CN3/4

CN7

DISPLAY
(1/2 82205)

CN5/6

DISLE[1..12]

DISRI[1..12]

CN4/5

MASTERBUS MA_BUS[1..16]

RDP_BU[1..16]

CN2

16-(NC)
15-(IN)
14-(+15V)
13-(GND)
12-(GND)
11-(+5V)
10-(S)
9-(S)
8-(S)
7-(S)
6-(+5V)
5-(GND)
4-(GND)
3-(OUT-R)
2-(-15V)
1-(OUT-L)

RDP 1A (80391)

1-(REL-DRIVE)
2-(+15V)
3-(+15V)
4-(GND)
5-(GND)
6-(GND)
7-(-15V)
8-(-15V)
CN4

POWER[1..8]

POWER ON

CN5

CN6

MAINS FUSE

POWER
CORD

ALTERATIONS RESERVED!

SYMB.	REVISION	DATE	NAME

Last modified: 07.04.1994 08:38:52

Last plotted: 17.09.1999 11:35:05

	DATE	NAME
DSG'D		
CHK'D		
APP'D		

DYNACORD

P5-WIRE

CIRCUIT DIAGRAM — 1/1

PowerMate 500 — 3-